

<b>FORM PTO 1449</b> <b>U.S. DEPARTMENT OF COMMERCE</b> <b>PATENT AND TRADEMARK OFFICE</b> <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> (On several sheets if necessary)					<b>File No.</b> YOR920040079US1	<b>Serial No.</b> 10/709,128
<div style="border: 1px solid black; border-radius: 50%; padding: 10px; display: inline-block; transform: rotate(-15deg);"> <b>PATENT &amp; TRADEMARK OFFICE</b>  <b>APR 29 2004</b> </div>					<b>Applicant(s):</b> Franaszek et al.	
<b>Filing Date:</b> April 15, 2004					<b>Group:</b> N/A	

  

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EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
LI	1	4,807,110	02/21/1989	Pomerene et al.	711	213	04/06/1984
LI	2	5,361,391	11/01/1994	Westberg	395	425	06/22/1992
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LI	1	EP0173893 A2	03/12/1986	European			
LI	2	JP8161230	06/21/1996	Japan			

  

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Patents, etc.)		
LI	1	Adaptive Variation of the Transfer Unit in a Storage Hierarchy, IBM J. Res. Develop., Vol. 22, No.4, July 1978, pp. 405-412 - by P.A. Franaszek and B. T. Bennett.
LI	2	Distributed Prefetch-buffer/Cache Design for High Performance Memory Systems, pp 254-263, Departments of Computer Science and Electrical Engineering, Duke University, Durham, NC, 1996 - by Thomas Alexander and Gershon Kedem.
LI	3	DRAM-Page Prediction and Prefetching, pp. 267-275, Computer Science Department, Duke University, Durham, NC, 2000 by Haifeng Yu and Gershon Kedem.
LI	4	On the Stability of Temporal Data Reference Profiles, Microsoft Research, Redmond, WA - by Trishul M. Chilimbi
LI	5	TCP: Tag Correlating Prefetchers, by Zhigang Hu of IBM Corp.; Margaret Martonosi of Princeton University; and Stefanos Kaxiras of Agere Systems.
LI	6	Performance Study of the Filter Data Cache on a Superscalar Processor Architecture, by Julio Sahuquillo, Salvador Petit and Ana Pont of Universidad Politecnica de Valencia, Spain and Veljko Milutinovic of University of Belgrade, Yugoslavia.
LI	7	A Data Cache with Multiple Caching Strategies Tuned to Different Types of Locality, by Antonio Gonzalez, Carlos Aliagas and Mateo Valero of Universitat Politecnica de Catalunya, Barcelona, Spain.
LI	8	Filtering Superfluous Prefetches using Density Vectors by Wei-Fen Lin, Steven K. Reinhardt of University of Michigan; Doug Burger of University of Texas at Austin; and Thomas R. Puzak of IBM Corporation.
LI	9	Page Fault Behavior and Prefetching in Software DSMs, by Ricardo Bianchini, Raquel Pinto, and Claudio L. Amorim of Federal University of Rio de Janeiro, Brazil, Technical Report ES-401/96, July 1996.
LI	10	Adaptive Caching for Demand Preparing, by Scott F. Kaplan, Lyle A. McGeoch, and Megan F. Cole of Amherst College, Massachusetts - ISMM '02, June 20-21, 2002, Berlin Germany
LI	11	Time Series Prediction using Recurrent SOM with Local Linear Models, Research Reports B15, Oct. 1997 - by Timo Koskela, Markus Varsta, Jukka Heikkonen, and Kimmo Kashi of Helsinki University of Technology, Finland.
LI	12	Temporal Sequence Processing using Recurrent SOM, by Timo Koskela, Markus Varsta, Jukka Heikkonen, and Kimmo Kaski of Helsinki University of Technology, Finland.

  

<b>Examiner</b> <div style="text-align: right;">/Lev Iwashko/</div>	<b>Date considered</b> (04/26/2006)
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**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.